

**AMENDMENTS TO THE CLAIMS:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**LISTING OF CLAIMS:**

1. (Previously Presented) An image processor which processes multi-level image data on density levels of pixels, comprising:

an edge judgement circuit which discriminates an edge direction of a target pixel from differences in density level between the target pixel and adjacent pixels thereof based upon the multi-level image data; and

a density level determining circuit which determines multi-level density levels in a plurality of sub-pixels in the target pixel, where the target pixel is divided into the sub-pixels, in accordance with the density level of the target pixel and the edge direction of the target pixel discriminated by the edge judgment circuit.

2. (Original) The image processor according to claim 1, wherein the density level determining circuit comprises:

a density controller circuit which sets density-level setting parameters for each of the sub-pixels in the target pixel in accordance with the edge direction of the target pixel discriminated by said edge judgment circuit; and

a density-level setter circuit which sets the density level of each of the plurality of sub-pixels in the target pixel based upon the density level of the target pixel by using the parameters set by said density controller circuit.

3. (Original) The image processor according to claim 1, wherein said edge judgment circuit discriminates a first edge which represents that an edge of a character image exists in a first direction relative to the target pixel, a second edge which represents that an edge of a character image exists in a second direction opposite to the first direction relative to the target pixel, and a narrow edge which represents that a character image exists at a center of the target pixel.

4. (Original) The image processor according to claim 1, wherein said edge judgment circuit cancels the discriminated edge direction when the density level of a pixel adjacent to the target pixel in the edge direction is larger than a threshold value.

5. (Original) The image processor according to claim 1, further comprising:  
a line width judgment circuit which determines a width of a line including the target pixel;

a smoothing circuit which performs smoothing on the image data of the target pixel and the adjacent pixels thereof in accordance to the line width determined by

said line width judgment circuit and outputs the image data of the target pixel which have been smoothed;

wherein the density level determining circuit determines the density levels in the plurality of sub-pixels in accordance with the density level of the target pixel subjected to smoothing by said smoothing circuit and the edge direction of the target pixel discriminated by the edge judgment circuit.

6. (Original) The image processor according to claim 5, wherein the density level determining circuit comprises:

a density controller circuit which sets density-level setting parameters for each of the sub-pixels in the target pixel in accordance with the edge direction of the target pixel discriminated by said edge judgment circuit; and

a density-level setter circuit which sets the density level of each of the plurality of sub-pixels in the target pixel based upon the density level of the target pixel by using the parameters set by said density controller circuit.

7. (Original) The image processor according to claim 6, wherein said line-width judgment circuit determines the line width of the line including the target pixel based upon the edge direction of the target pixel and those the adjacent pixels of the target pixel discriminated by the edge judgment circuit.

8. (Original) The image processor according to claim 1, further comprising an edge judgment correction circuit connected to said edge judgment circuit and corrects the edge direction when the edge direction discriminated by said edge judgment circuit is not appropriate.

9. (Original) The image processor according to claim 8, wherein said density level determining circuit comprises:

a density controller circuit which sets parameters for each of the sub-pixels based on the edge direction discriminated by said edge judgment circuit or by said edge judgement correction circuit; and

a density-level setter circuit which sets a density level for each of a plurality of sub-regions in the target pixel based upon the density level of the target pixel by using the parameters set by said density controller circuit.

10. (Original) The image processor according to claim 1 further comprising a smoothing circuit which performs smoothing on image data of the pixel, on which said edge judgment circuit discriminates an edge, by using an asymmetric filter having the target pixel at a center thereof, wherein said density level determining circuit determines density level of each of the sub-pixels in the target pixel based on the density level of the image data of the target pixel smoothed by said smoothing circuit and on the edge direction of the target pixel discriminated by said edge judgment circuit.

11. (Original) The image processor according to claim 10, wherein said density level determining circuit comprises:

a density controller circuit which sets parameters for each of the sub-pixels based on the edge direction discriminated by said edge judgment circuit or by said edge judgement correction circuit; and

a density-level setter circuit which sets a density level for each of a plurality of sub-regions in the target pixel based upon the density level of the target pixel smoothed by said smoothing circuit by using the parameters set by said density controller circuit.'

12. (Original) The image processor according to claim 10, wherein said filter is asymmetrical with respect to a direction perpendicular to which a pixel is divided into sub-pixels.

13. (Original) The image processor according to claim 10, wherein said smoothing circuit comprises a plurality of filters and selects one of them for smoothing.

14. (Original) The image processor according to claim 10, wherein said smoothing circuit comprises a plurality of filters and selects one of the filters which provides a minimum density level of the target pixel after the smoothing carried by the filters.

15. (Currently Amended) A method for processing ~~proeesses~~ multi-level image data on density levels of pixels, where a pixel is divided into a plurality of sub-pixels, comprising the steps of:

discriminating an edge direction of a target pixel from differences in density level between the target pixel and adjacent pixels thereof based upon the multi-level image data; and

determining multi-level density levels in a plurality of sub-pixels in the target pixel in accordance with the density level of the target pixel and the discriminated edge direction of the target pixel.

16. (Original) The method according to claim 15, further comprising the steps of:

determining a width of a line including the target pixel; and

performs smoothing on the image data of the target pixel and the adjacent pixels thereof in accordance to the determined line width and outputs the image data of the target pixel which have been smoothed;

wherein in said step of determining density levels of determines the density levels in the plurality of sub-pixels in accordance with the density level of the target pixel subjected to smoothing and the discriminated edge direction of the target pixel.